



**S/N 10/657,681**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Dudi Amir	Examiner:	Ren Yan
Serial No.:	10/657,681	Group Art Unit:	2854
Filed:	September 8, 2003	Docket:	884.147US2
Title:	METHODS FOR MANUFACTURING PRINTED CIRCUIT BOARDS USING A PARTIAL PRINTING PROCESS AND APPARATUS		
Assignee:	Intel Corporation	Customer No:	21186

---

**TERMINAL DISCLAIMER**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I, Ann M. McCrackin, am the attorney of record for the above identified patent application as evidenced by the Power of Attorney filed in the present application on September 8, 2003. I am making this petition on behalf of Intel Corporation, the assignee of the present invention. As the attorney of record, I am empowered to act on behalf of the assignee and, in accordance with 37 C.F.R. § 1.321(b)(iv), to sign this terminal disclaimer.

**Certificate Under 37 C.F.R. § 3.73(b)**

Your petitioner, Intel Corporation, certifies that they are the owner of the entire right, title and interest in and to the above-identified patent application (Serial No. 10/657,681) and to U.S. Patent No. 6,626,099. Your petitioner owns the entire right, title, and interest of these applications by nature of the assignments executed and filed for both of these applications. The assignment for U.S. Patent No. 6,626,099 was recorded on November 5, 1999 on Reel 010378, Frames 604-606, with the United States Patent and Trademark Office. The above-identified patent application (Serial No. 10/657,681) is a divisional of U.S. Patent No. 6,626,099.

The undersigned representative of the assignee has reviewed the evidentiary documents of title and certifies that to the best of assignee's knowledge and belief, title is in the assignee, Intel Corporation, seeking to take the action set forth in this disclaimer.

**TERMINAL DISCLAIMER**

Serial Number: 10/657,681

Filing Date: September 8, 2003

Title: METHODS FOR MANUFACTURING PRINTED CIRCUIT BOARDS USING A PARTIAL PRINTING PROCESS AND APPARATUS

Assignee: Intel Corporation

**Page 2**

Dkt: 884.147US2 (INTEL)

**Terminal Disclaimer**

Your petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the above-identified patent application, which would extend beyond the expiration date of the full statutory term, as presently shortened by any terminal disclaimers, of U.S. Patent No. 6,626,099. Petitioner hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to U.S. Patent No. 6,626,099 and the legal title of the above-identified application and any patent granted thereon remain common, this agreement to run with any patent granted on the above-identified application and to be binding upon the grantee, its successors and assigns.

**Limitations on the Disclaimer**

Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application prior to the expiration date of the full statutory term as presently shortened by any terminal disclaimer of U.S. Patent No. 6,626,099 in the event that it later expires for failure to pay a maintenance fee, is held unenforceable, is found invalid, is statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), has all claims canceled by a reexamination certificate, or is otherwise terminated prior to the expiration date of its statutory term as presently shortened by any terminal disclaimer, except for the separation of legal title as stated hereinabove.

TERMINAL DISCLAIMER

Serial Number: 10/657,681

Filing Date: September 8, 2003

Title: METHODS FOR MANUFACTURING PRINTED CIRCUIT BOARDS USING A PARTIAL PRINTING PROCESS AND APPARATUS

Assignee: Intel Corporation

Page 3

Dkt: 884.147US2 (INTEL)

Fee Status

The Commissioner of Patents and Trademarks is authorized to charge Deposit Account 19-0743 in the amount of \$110.00, which is required under 37 C.F.R. § 1.20(d) to file a statutory disclaimer. The Commissioner is hereby authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

DUDI AMIR

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Attorneys for Intel Corporation

P.O. Box 2938

Minneapolis, Minnesota 55402

(612) 349-9592

Date June 7, 2004

By Ann M. McCrackin  
Ann M. McCrackin  
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 7 day of June, 2004.

KACIA LEE  
Name

Kacia Lee  
Signature